
Digital Trunk/Primary Rate Interface (LD 60)

LD 60 is used to maintain the following:

- QPC471/775 Clock Controller
- QPC472 1.5 Mb/s Digital Trunk Interface (DTI)
- QPC536 2.0 Mb/s Digital Trunk Interface (DTI2)
- QPC720 Primary Rate Interface (PRI)

This diagnostic program can be can be run in midnight routines or loaded manually to enter commands. The following lists describe the commands available with LD 60.

DTI and PRI commands

ATLP (0), 1	Disable (0) or enable (1) daily routine auto loop test
CDSP	Clear maintenance display on the active CPU
CMIN ALL	Clear minor alarm indication on all attendant consoles
CMIN c	Clear minor alarm indication on attendant consoles for customer c
DISI loop	Disable DTI/PRI loop when all channels are idle
DISL loop	Disable network loop (LD 17 DLOP) and associated DTI/PRI cards
DLBK loop	Disable remote loopback test previously set by RLBK loop command
DLBK loop ch	Disable remote loopback test previously set by RLBK loop ch command
DSCH loop ch	Disable channel ch of DTI/PRI loop
ENCH loop ch	Enable channel ch of DTI/PRI loop
ENCH loop	Enable all channels on loop (DTI2 only)
ENLL loop	Enable network loop (LD 17 DLOP) and associated DTI/PRI cards
LCNT (loop)	List contents of alarm counters on one or all DTI/PRI loops
LOVF c r	List threshold overflows for customer c (0-99) and route r (0-511)
RCNT	Reset alarm counters of all DTI/PRI loops
RCNT loop	Reset alarm counter of DTI/PRI loop
RMST loop	Perform self-test on specified loop
RMST loop ch	Perform self-test on specified channel (DTI2 only)
RLBK loop	Close loop at carrier interface point for testing
RLBK loop ch	Close channel at carrier interface point

RSET loop ch	Reset thresholds for channel ch
SLFT loop	Invoke DTI/PRI hardware self-test on loop
SLFT loop ch	Invoke partial hardware self-test on channel ch
STAT	Get status of all PRI loops
STAT loop	Get status of PRI loop
STAT loop ch	Get status of channel ch

Clock Controller commands

DIS CC 0, 1	Disable system Clock Controller 0 or 1
DSYL loop	Disable yellow alarm processing for specified loop
ENL CC 0, 1	Enable system Clock Controller 0 or 1
ENYL loop	Enable yellow alarm processing for specified loop
EREF	Enable automatic switchover of system clocks
MREF	Disable switchover of system clocks
SSCK 0, 1	Get status of system clock 0 or 1
SWCK	Switch system clock from active to standby
TRCK aaa	Set Clock Controller tracking to primary, secondary or free run

